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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/740,034	12/18/2003	Yoshiya Hirasc	883.0006.U1(US)	2513
	7590 10/26/2007 N & SMITH, PC		EXAMINER	
4 RESEARCH	DRIVE	ZHE, MENG YAO		
SHELTON, CT 06484-6212			ART UNIT	PAPER NUMBER
			2195	
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			10/26/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

		Applica	ation No.	Applicant(s)				
		10/740	,034	HIRASE, YOSHIY	′ A			
Office Action Summary			ner	Art Unit				
		MengY	ao Zhe	2195				
Period fo	The MAILING DATE of this commun r Reply	ication appears on	the cover sheet wi	th the correspondence ac	idress			
WHIC - Exter after - If NO - Failu Any r	ORTENED STATUTORY PERIOD F HEVER IS LONGER, FROM THE M Isions of time may be available under the provisions SIX (6) MONTHS from the mailing date of this comm period for reply is specified above, the maximum st- e to reply within the set or extended period for reply eply received by the Office later than three months and patent term adjustment. See 37 CFR 1.704(b).	IAILING DATE OF of 37 CFR 1.136(a). In no nunication. atutory period will apply an will, by statute, cause the	THIS COMMUNIC event, however, may a red d will expire SIX (6) MON application to become AB	CATION. eply be timely filed THS from the mailing date of this of the company o				
Status								
1)	Responsive to communication(s) file	ed on 18 December	r 2003.					
,	This action is FINAL . 2b)⊠ This action is non-final.							
, —	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is							
,	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.							
Dispositi	on of Claims		1		•			
4) 又	Claim(s) 1-17 is/are pending in the a	application.						
	4a) Of the above claim(s) is/are withdrawn from consideration.							
5)[5) Claim(s) is/are allowed.							
6)⊠	Claim(s) 1-17 is/are rejected.							
7)	Claim(s) is/are objected to.							
8)□	Claim(s) are subject to restrict	ction and/or election	n requirement.					
Applicati	on Papers		1					
9)	The specification is objected to by th	e Examiner.						
10)	The drawing(s) filed on is/are:	a) accepted or	b) objected to	by the Examiner.				
	Applicant may not request that any obje	ction to the drawing(s) be held in abeyar	nce. See 37 CFR 1.85(a).				
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).								
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.								
Priority u	nder 35 U.S.C. § 119							
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of:								
	1. Certified copies of the priority documents have been received.							
	2. Certified copies of the priority documents have been received in Application No							
3. Copies of the certified copies of the priority documents have been received in this National Stage								
application from the International Bureau (PCT Rule 17.2(a)).								
* See the attached detailed Office action for a list of the certified copies not received.								
Attachmen			. —					
	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (F	PTO-948)		Summary (PTO-413) s)/Mail Date				
3) 🔯 Inforr	nation Disclosure Statement(s) (PTO/SB/08) r No(s)/Mail Date 7/15/2004.	. 5 6 . 6,	5) D Notice of I	Notice of Informal Patent Application				

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DETAILED ACTION

1. Claims 1-17 are presented for examination.

Double Patenting

2. A rejection based on double patenting of the "same invention" type finds its support in the language of 35 U.S.C. 101 which states that "whoever invents or discovers any new and useful process ... may obtain a patent therefor ..." (Emphasis added). Thus, the term "same invention," in this context, means an invention drawn to identical subject matter. See *Miller v. Eagle Mfg. Co.*, 151 U.S. 186 (1894); *In re Ockert*, 245 F.2d 467, 114 USPQ 330 (CCPA 1957); and *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970).

A statutory type (35 U.S.C. 101) double patenting rejection can be overcome by canceling or amending the conflicting claims so they are no longer coextensive in scope. The filing of a terminal disclaimer <u>cannot</u> overcome a double patenting rejection based upon 35 U.S.C. 101.

3. Claims 1-17 are provisionally rejected under 35 U.S.C. 101 as claiming the same invention as that of claims 1-17 of copending Application No. 10740036. This is a provisional double patenting rejection since the conflicting claims have not in fact been patented.

Claim Rejections - 35 USC § 101

4. 35 U.S.C. 101 reads as follows:

Whoever invents or discovers any new and useful process, machine, manufacture, or composition of matter, or any new and useful improvement thereof, may obtain a patent therefor, subject to the conditions and requirements of this title.

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Claims 1-6, and 13-17 are rejected because the claimed invention, appearing to be comprised of <u>software alone</u> without claiming associated <u>computer hardware</u> required for execution, is not supported by either a specific and substantial asserted utility (i.e., transformation of data) or a well established utility (i.e. a practical application).

Claim Rejections - 35 USC § 112

5. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

- 6. Claims 1-17 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.
 - A. The following claim languages are unclear and indefinite:
 - i) Claim 1, line 3, it is unclear what "a Dynamic Configurable Hardware Logic layer" is <i.e. how is it different from any device or hardwares that are attached to a computer?>.

lines 5-6, it is unclear what a "TiEred Multi-midia Acceleration Scheduler" is <i.e. how is it different from any other schedulers?>.

Claims 7 and 13 have the same deficiencies as claim 1 above.

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ii) Claim 2, it is unclear what a "Tier-1 scheduler" and a "Tier-2 scheduler" are and how they are different from each other <i.e. what are their functions? How are they distinguished from other schedulers and each other?>.

Claims 8 and 14 have the same deficiencies as claim 2 above.

iii) Claim 4, it is uncertain as to what is meant by "one context plane" <i.e. what defines a context plane?>.

Claims 10 and 16 have the same deficiencies as claim 4 above.

Claim Rejections - 35 USC § 102

7. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 8. Claims 1 and 7 are rejected under 35 U.S.C. 102(e) as being anticipated by Karam et al., Patent No. 7,111,089 (hereafter Karam).

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9. As per claims 1 and 7, Karam teaches a device architecture for running applications, comprising:

an operating system (OS) comprising an OS scheduler (Fig 1, units 22 and 31);

a Dynamic Configurable Hardware Logic (DCHL) layer comprised of a plurality of Logic Elements (LEs) (Fig 1, units 20 and 21);

and interposed between said OS and said DCHL layer, a TiEred Multi-media Acceleration Scheduler (TEMAS) that cooperates with the OS scheduler for scheduling and configuring the LEs of the DCHL to execute applications (Fig 1, unit 30; Column 2, lines 49-63).

Claim Rejections - 35 USC § 103

- 10. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 11. Claims 1-17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hoskins, Patent No. 6,789,132 in view of Kaihlaniemi, Patent No. 6,370,591 (hereafter Kaihlaniemi).

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12. As per claims 1 and 7, Hoskins teaches a device architecture for running applications, comprising:

a Dynamic Configurable Hardware Logic (DCHL) layer comprised of a plurality of Logic Elements (LEs) (Fig 2, unit 110 and all of unit 202 except unit 222);

and interposed between a host computer (Fig 2, unit 200) and said DCHL layer, a TiEred Multi-media Acceleration Scheduler (TEMAS) (Fig 2, unit 222) that cooperates with the host computer for scheduling and configuring the LEs of the DCHL to execute applications (Column 5, lines 35-39; Column 6, lines 1-10, lines 41-47).

Hoskins does not specifically teach that the host computer has an operating 13. system comprising an OS scheduler that the TEMAS cooperates with.

However, Kaihlaniemi teaches personal computers running operating systems that are able to communicate with external devices (Column 1, lines 11-33) for the purpose of controlling devices using operating systems. It would have been obvious to one having ordinary skill in the art at the time of the applicant's invention to have combined the teachings of Hoskins with the host computer has an operating system comprising an OS scheduler that the TEMAS cooperates with, as taught by Kaihlaniemi, because it allows for the operating system to control devices.

As per claims 2, 8, 14, Hoskins teaches where the TEMAS is comprised of a Tier-1 scheduler that communicates with the OS scheduler and at least one Tier-2

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scheduler (Fig 3, units 303, 305, and 307) interposed between the Tier-1 scheduler and one DCHL configurable device (Column 6, lines 50-57; Column 8, lines 39-44).

- 15. As per claims 3, 9, 15, Hoskines teaches where the TEMAS operates in response to configuration requests to configure and reconfigure at least some of the plurality of LEs in accordance with at least one algorithm logic (Column 7, line 1-9; Column 8, lines 39-64; Column 9, lines 10-21).
- 16. As per claims 4, 10, 16, Hoskins teaches where said plurality of LEs are disposed within at least one context plane (Fig 2, unit 110 and all of unit 202 except unit 222).
- 17. As per claims 5, 11, Hoskins teaches an application layer that comprises at least one application (Column 20, lines 34-37), a service layer that comprises said Tier-1 scheduler (Fig 2, unit 222), a node layer that comprises said at least one Tier-2 scheduler that is coupled to a scheduling algorithm of said Tier-1 scheduler (Fig 3, units 303, 305, 307), and a hardware layer that comprises said at least one DCHL configurable device (unit 110 and unit 212).

Kaihlaniemi teaches an operating system with scheduler (Column 1, lines 12-28)

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18. As per claim 6, 12, Kaihlaniemi teaches where said device comprises a device having wireless communications capability (Column 1, lines 48-50).

19. As per claim 13, Hoskins teaches an applications layer comprising a plurality of applications (Column 20, lines 34-37); a hardware layer comprising Dynamic Configurable Hardware Logic (DCHL) comprised of a plurality of Logic Elements (LEs) (unit 110 and unit 212); and interposed between host computer and said DCHL in said service layer and in a node layer, a TiEred Multi-media Acceleration Scheduler (TEMAS) that cooperates with the host computer for scheduling and configuring the LEs of the DCHL to execute said applications (Column 5, lines 35-39; Column 6, lines 1-10, lines 41-47).

Kaihlaniemi teaches personal computers running operating systems that are able to communicate with external devices (Column 1, lines 11-33) and a wireless communication device (Column 1, lines 48-50).

20. As per claim 17, Kaihlaniemi teaches where said device comprises a cellular telephone (Column 1, lines 48-50).

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Conclusion

21. Any inquiry concerning this communication or earlier communications from the examiner should be directed to MengYao Zhe whose telephone number is 571-272-6946. The examiner can normally be reached on Monday Through Friday, 7:30 - 5:00 EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Meng-Ai An can be reached at 571-272-3756. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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